ENEL 387

Functional Specifications

Variable Speed Drive

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# 1. System Information

## 1.1 Description

The Variable Speed Drive (VSD) allows the user to control the average voltage delivered to a load using a microprocessor to control the firing angle of a TRIAC. Applications for the VSD include variable heat delivery from a heating element, light dimming applications, and variable DC motor speed control. The VSD operates at 120VAC with a maximum current rating of 15A and is connected using a typical 5-15R male corded end to the supply and a 5-15R female end to supply power to the load. The microprocessor system is easily scaled to higher voltage or current applications by replacing the power electronics section with another appropriately rated device. The device also includes a test point to inspect the resulting waveform and the instrumentation necessary to display load parameters such as voltage and current. All connections between the microprocessor and the power electronics are isolated magnetically through the use of a PT or CT or optically through the use of an opto-isolator. All high voltage equipment is contained in an appropriate box and wired according to CSA standards.

## 1.2 Diagram

The basic system components are shown below. The system contains two major sections, the microprocessor section and the power electronics section. The user interface is included in the microprocessor section.

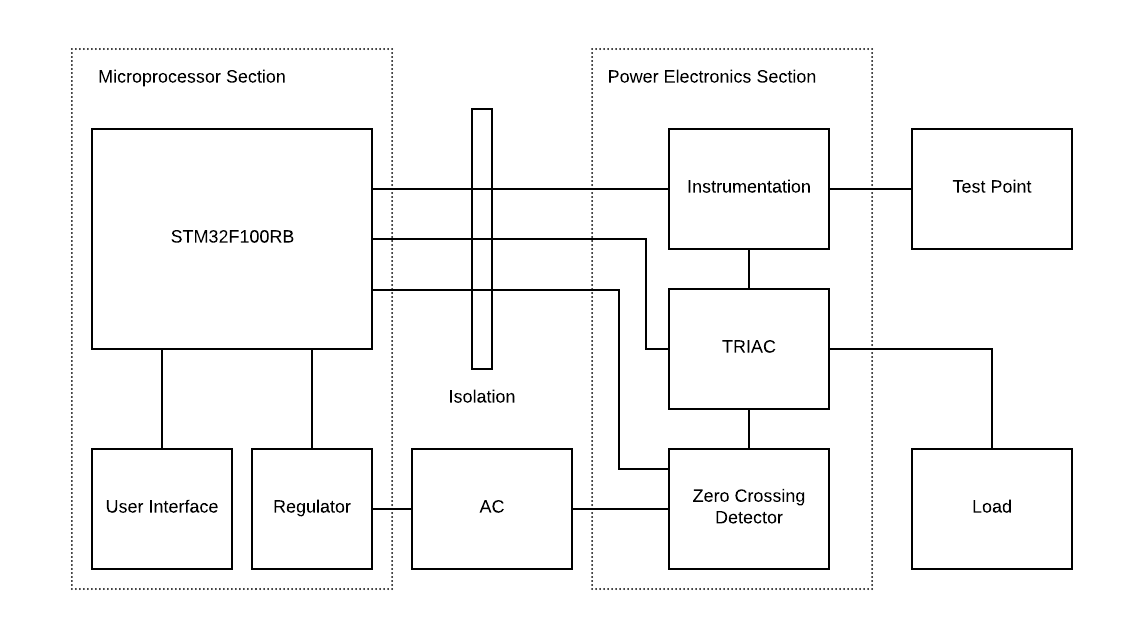


Figure 1 - Block Diagram of VSD

## 1.3 Physical Layout

The VSD contains two major sections, the Microprocessor section and the Power Electronics section. The microprocessor section contains the STM32F100RB microprocessor, the low voltage power supply, and the user interface. The user interface gives the user access to indicator LED’s, pushbuttons, a setpoint dial, and a 16x2 LCD screen:

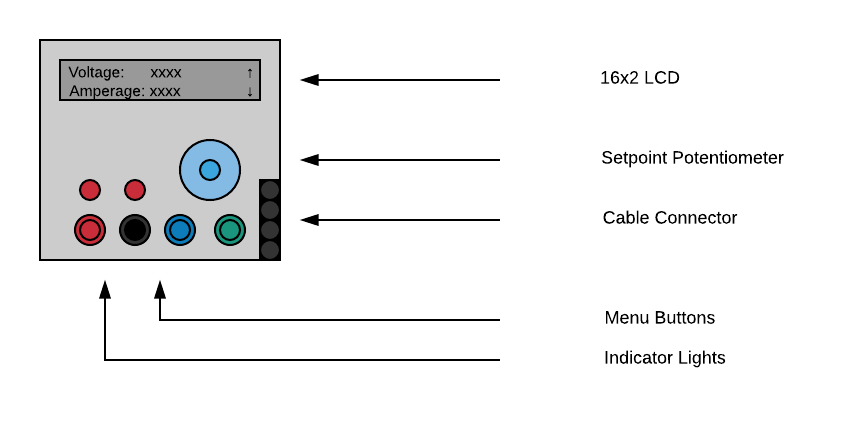


Figure 2 - User Interface Layout

The Power Electronics section contains the zero-crossing detector circuit, the TRIAC firing circuit, and the instrumentation and test circuit. The Power Electronics section is housed in a CSA approved box rated for 120V. In addition to the Power Electronics circuitry the box also contains an appropriate means of controlling over-current and the receptacles needed to connect the VSD to the load. Communication between the two sections is accomplished using a ribbon cable with appropriate isolation between high-voltage and low-voltage.

## 1.4 Applications

The major applications of the VSD include variable heating, light dimming, and variable DC speed control. By controlling the average voltage dropped across a heating element, the power dissipated by that element is also reduced. This can be used to reduce the heat output of a typical resistive heating element and provide an alternative to an ON-OFF thermostat control. Similarly, the light produced by a standard incandescent bulb is proportional to the power dissipated by the bulb. By changing the power at the bulb, the light that the bulb outputs is also changed. Finally, the speed of a DC motor is proportional to the voltage across the armature of the motor. By placing an appropriate bridge rectifier circuit at the output of the VSD the DC voltage delivered to the motor can be controlled, which in turn controls the speed of the motor.

## 1.5 Electrical Ratings

Operating Ratings:

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Rating | | Min | Typ | Max | Notes |
| Supply Voltage | VAC | 100 | 120 | 130 | -Appropriate 15A branch circuit |
| Amperage | A |  | 0 - 5 | 10 | -Continuous Draw |
| Test Output | VAC | 8.33 | 10.0 | 10.8 | -10VA Max loading |

# 2. Operation

## 2.1 Start Up

The VSD enters a start-up sequence when first powered up by connecting the box to a 120VAC wall outlet. During this sequence all functions are disabled and the TRIAC circuit will be de-energized. Once this sequence is complete the LCD will show that the drive is ready to start.

## 2.2 Normal Operation

After the start-up sequence the VSD enters an Idle state, where it waits for user input. During this stage the user can traverse the menu, change the output setpoint, and start the VSD. The VSD is started by selecting the start option in the menu. Selecting the start option transitions the VSD into the Running state, where the output is enabled and controlled by the setpoint dial. During this state an LED is energized to show the user that the high voltage section of the VSD is energized. During this state the user can view parameters such as the setpoint, output voltage, and output current on the LCD. At any time the user can hit the stop button to de-energize the output and transition the VSD back to the Idle state.

In the event of a fault condition, the VSD transitions into a Fault state, where it gathers information about the fault and displays it on the LCD for the user. In this state an LED is flashed to show the user that the VSD is faulted. In order to clear this state, the condition which caused the fault must be cleared and power to the VSD must be cycled before operation may continue. The list of active faults may be viewed on the LCD and cycled through using the menu buttons. Refer to section 2.3 for a full list of fault conditions.

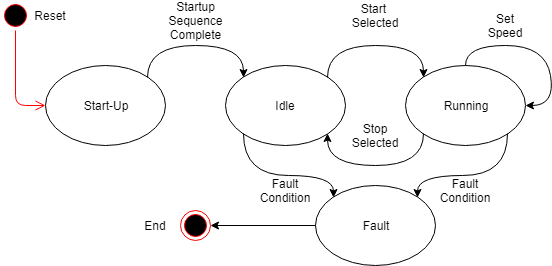


Figure 3 - VSD State Transitions

## 2.3 Faults

Faults fall into one of two categories, EIC and EOC. An EIC fault is an internal logic fault, often caused by improper input to the device or faulty equipment in the Microprocessor section. An EOC fault is an output fault, often caused by the load being faulted, not properly sized, or faulty equipment in the Power Electronics section. The first step in clearing an EIC fault is to cycle power to the device. If the fault occurs again repairs may need to be made to the Microprocessor section or a firmware update may be required. The first step in clearing an EOC fault is to determine if the load is in good working order. If the load is not faulted and the fault occurs again repairs may need to be made to the Power Electronics section. A full list of faults and fault codes can be found below:

|  |  |  |  |
| --- | --- | --- | --- |
| Code: | Name: | Causes: | Solution |
| EIC-00 | Internal Software Fault | -Fault in internal logic | -Cycle power to VSD |
| EIC-01 | Start-up Sequence Fault | -Fault in start-up sequence | -Cycle power to VSD |
| EIC-10 | User Input | -Wrong input on menu pushbuttons | -Cycle power to VSD |
| EIC-11 | Setpoint Out of Bounds | -Setpoint input is out of bounds | -Setpoint dial is not connected to appropriate input |
| EIC-30 | Reserved for Future Use |  |  |
| EIC-40 | Reserved for Future Use |  |  |
| EIC-50 | Reserved for Future Use |  |  |
| EOC-00 | Faulted Drive | -Did not see expected voltage/current output | -Ensure load is connected  -Ensure CT is connected properly and not faulted  -Ensure PT is connected properly and not faulted  -Replace Power Electronics circuitry |
| EOC-01 | ZFC Input Not Detected | -Did not see expected input from Zero Crossing Detector Module | -Ensure ZFC - PT is working properly  -Replace ZFC module |
| EOC-10 | Overcurrent | -Current to load above maximum safe value | -Ensure load is not faulted and properly sized for drive |
| EOC-20 | Reserved for Future Use |  |  |
| EOC-30 | Reserved for Future Use |  |  |
| EOC-40 | Reserved for Future Use |  |  |
| EOC-50 | Reserved for Future Use |  |  |

# 3. Design

Building the VSD required the design of several modules, including the selection of the individual components and the design of the zero-crossing circuit, instrumentation circuit, TRIAC firing circuit, and TRIAC high voltage circuit. In addition, all of these components had to be interfaced with the microcontroller and the drivers required to operate these components needed to be designed and tested. The design of the individual components is detailed in the following subsections.

## 3.1 TRIAC Circuit

The TRIAC circuit design was difficult due to the 120VAC required at the TRIAC and the need for isolation between the high voltage and the microcontroller. The isolation was accomplished using a secondary opto-TRIAC, which is an optically isolated TRIAC. The opto-TRIAC has four pins, two input pins that drive an internal LED and two output pins that tie to an internal TRIAC. The internal TRIAC is fired using the low voltage LED, which gives the required isolation between low voltage and high voltage. At the low voltage side the microcontroller output is tied to the opto-TRIAC using a typical LED current limiting circuit. At the high voltage side the opto-TRIAC is tied to the gate of the power TRIAC using a high wattage resistor. The high voltage circuit is designed so that the opto-TRIAC pins are at the same potential as the power TRIAC, so when the TRIAC is idle the voltage across the opto-TRIAC is 120VAC but the current is zero and when the TRIAC is conducting the voltage across the opto-TRIAC is the saturation voltage of the power TRIAC (~0.15V). This means that the resistors only need to be sized to limit the peak current through the opto-TRIAC to a safe amount, but the power dissipation of the resistors is negligible because they only see meaningful current and voltage for a brief moment when the TRIAC is fired. The power dissipation for this moment is large, but the time-average over the whole 8.33ms cycle is very small and the large wattage of the resistors is redundant.

The high voltage circuit is built on a proto-board with sufficient space to hold the two TRIACS and the resistors without risk of a short developing between the 120VAC circuit and the low voltage circuit. In addition, special care was taken to follow creepage/clearance rules for high voltage prototyping. The clearance between two traces is the closest distance between them and relates to the ability of an arc to form between components that are too close. Creepage is the distance between two traces following the surface of the PCB and any obstructions, and relates to the ability of a arc to form between components if a breakdown of the boards insulation occurs. For a 120VAC circuit, it was sufficient to place an unused trace between two 120VAC traces to adhere to the creepage/clearance guidelines.

## 3.2 Zero-Crossing Module

The zero-crossing module is used by the system to sync the TRIAC firing to the AC waveforms zero crossings. The module sends a square wave to the microcontroller with edges that correspond to the AC waveforms positive and negative zero crossing. The microcontroller generates an interrupt on the rising and falling edge of that signal that is used to reset the counter used for the TRIAC firing. The module accomplishes this by first transforming the 120VAC voltage down to ~10VAC using a potential transformer. It then uses a resistor-divider to drop the voltage down to an acceptable level and a single supply op-amp as a comparator to generate the square wave.

## 3.3 Instrumentation Circuit

The instrumentation circuit is responsible for conditioning the output of the CT to a level acceptable for the microcontroller to read. The circuit implements a peak-detector to output a dc value that corresponds to the peak value of the current. The peak value is scaled in the microcontroller using a scaling factor of 0.707. Due to the fact that the output of the TRIAC is not sinusoidal, this circuit was not able to accurately report the waveform and was not implemented in the final project.

Another implementation of this circuit involved a dual-supply Op-Amp used to amplify the output of the CT and a precision rectifier circuit used to cut the negative half of the waveform. This would be sampled by the microcontroller and an average current would be calculated using the individual samples taken during the cycle. This was not implemented in the final project due to the noise associated with the TRIAC. It was found that the calculated value was not accurate and varied over a wide range due to high frequency noise found at the TRIAC output.

## 3.4 Deviation from Functional Specification

The project deviates from the original functional specifications in some ways. The largest difference is the absence of the current transformer in the finished product. The originally designed CT conditioning circuit was designed to work with a sinusoidal input waveform, which did not work with the output of the TRIAC. A few other methods were attempted to read the current waveform, such as averaging the values taken across the whole cycle. The alternative methods attempted failed due to how noisy the current waveform is when the TRIAC is firing in the middle of the cycle. In the finished product, the current is approximated using the setpoint percentage and a hard-coded maximum current value.

Due to a lack of current feedback several of the fault codes initially put forth in the functional specs had to be ignored, such as overcurrent, overload, and drive fail. Only half of the suggested drive faults were implemented in the finished product. Finally, the ability for the user to enter a maximum current for the load using the menu and pushbuttons was not implemented due to a lack of current feedback.

# 4. Testing

The testing procedure for this system consisted of several steps, from individual component testing to the full system evaluation. Due to the use of high voltages in the system, special care was taken to design safe testing strategies when testing the individual high-voltage components and the finished system. The initial testing of the individual components involved using the oscilloscope, function generator, power supply, and 120VAC wall supply to build test benches for each component. In this stage, each transformer was connected to 120VAC and the resulting voltage waveform was viewed with the oscilloscope. In addition, the TRIAC and opto-TRIAC were tested by simulated a firing circuit using the function generator and power supply and the output was viewed using the oscilloscope. The testing of the TRIACS was difficult due to the fact that the test equipment was not able to supply enough current to fire the main power TRIAC, so the opto-TRIAC was tested using the test bench and the main TRIAC was tested later using an isolated 45VAC supply.

The next step involved testing modules by constructing them on breadboards and viewing the output on the oscilloscope. The zero-crossing module was constructed on a breadboard and tested by connecting the potential transformer to a 120VAC supply and viewing the low voltage output of the detector on the oscilloscope. The instrumentation circuit was tested by simulating the waveform that would be expected from the CT using the function generator and viewing the result on the oscilloscope. Once the outputs of the individual modules were found to be within the required range, integration testing with the microcontroller began. Each circuit was connected to the microcontroller and debugger was used to test the component drivers. For the zero-crossing detector, a 1 Hz signal was set as the input into the detector and an LED was made to toggle as the interrupt was called. For the TRIAC firing circuit the input of the opto-TRIAC was connected to the microcontroller and the output was connected to a low-voltage test signal. The output of the TRIAC was viewed on the oscilloscope as the setpoint for the firing angle was varied from zero to full.

The full system testing required the use of 120VAC, so a 120VAC isolation transformer with a low amperage fuse was used to feed the high-voltage circuit and a 40W incandescent bulb was used as the test load. The full system testing involved testing the overall operation of the device and properly scaling in the analog inputs. Testing of the menu system and the fault system involved using the debugger to ensure that the code was operating properly during state changes and during faults. A dip switch was used to force fault conditions for testing the fault system.

# 5. Components

## 5.1 High Level Components

The following components make up each section of the VSD:

|  |  |
| --- | --- |
| Microprocessor Section | |
| STM32F100RB | -Microcontroller system, including all support circuitry |
| Regulator | -Power regulation circuit necessary to convert 120VAC to 5VDC and 3.3VDC for microcontroller and user interface |
| User Interface | -Contains pushbuttons, setpoint dial, LCD screen, indicator LED’s, and all support circuitry |
|  |  |
| Power Electronics Section | |
| ZFC Module | -Contains zero-crossing detector circuitry necessary to control firing angle of TRIAC circuit |
| TRIAC Module | -Contains main TRIAC and necessary firing circuitry, including a secondary optically coupled firing TRIAC |
| Instrumentation | -Contains potential and current transformers and necessary circuitry required to relay running conditions to the microprocessor and the test point. |
|  |  |

## 5.2 Bill of Materials

|  |  |  |  |
| --- | --- | --- | --- |
| Component: | Quantity: | Price: | Total: |
| STM32VL Discovery Board | 1 | $9.95 | $9.95 |
| 16x2 Character LCD | 1 | $15.95 | $15.95 |
| Tactile Button Assortment | 1 | $4.95 | $4.95 |
| Voltage Regulator - 5.0v | 1 | $0.95 | $0.95 |
| Voltage Regulator - 3.3v | 1 | $0.95 | $0.95 |
| 10kΩ Trimpot | 1 | $0.95 | $0.95 |
| LM358N Op-Amp | 2 | $0.95 | $1.90 |
| Screw Terminals | 3 | $0.95 | $2.85 |
| 2x5 Ribbon Cable | 2 | $1.50 | $3.00 |
| 2x5 Ribbon Female | 2 | $0.50 | $1.00 |
| Current Transformer | 1 | $9.95 | $9.95 |
| MOC3020 Opto-TRIAC | 1 | $0.68 | $0.68 |
| QJ6016RH4TP TRIAC | 1 | $3.06 | $3.06 |
| Pomona Test-Jacks | 1 | $2.77 | $2.77 |
| Potential Transformer | 2 | $15.61 | $31.22 |
| Wire-Wrap Socket | 2 | $1.96 | $3.92 |
| 30AWG Wire-Wrap Wire 100' | 1 | $17.62 | $17.62 |
| 1/4" Ready-Rod | 1 | $5.69 | $5.69 |
| 1/4" Particle Board 4x4 | 1 | $10.59 | $10.59 |
| Assorted Hardware | 1 | $10.00 | $10.00 |
| Assorted Components | 1 | $20.00 | $20.00 |
| Proto-Boards | 3 | $5.95 | $17.85 |
| Total: | 31 |  | $175.80 |